

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2037	718/102,106.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:08
L2	578814	(second near driver near depend\$3 near first driver)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/07 12:08
L3	327	l1 and L2	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:08
L4	1	l3 and EFI	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:09
L5	0	l1 and (bootstrap near logical)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:09
L6	37	l1 and (logical near processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:09
L7	11	l6 and (boot)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:10
L8	17	l1 and (driver near execut\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:10
L9	16	l8 and (sequence or queue or order)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/07 12:10
S1	2	"20040268108"	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 11:20
S2	1	"6158000".pn.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 14:28
S3	1	"5938765".pn.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 14:32

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S4	0	"2003009654".pn.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 14:32
S5	0	"2003009654"	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 14:33
S6	39	(nalawadi near rajeev).inv.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2008/01/03 14:33
S7	15	"6601167"	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:05
S8	1	"6601167".pn.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:08
S9	0	oot near loader	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:08
S10	1298	boot near loader	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:08
S11	62	S10 and (driver near execut\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:12
S12	440	bios and EFI	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:13
S13	101	S12 and (driver near execut\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	ON	2008/01/04 11:13
S14	3	US-20030110369-.DID. OR US-20030188173-.DID. OR US-20050204357-.DID.	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 13:21
S15	1	"5854905".pn.	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:09
S16	2007	driver near depend\$3	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:16

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S17	0	S16 and (first near driver near execut\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:14
S18	216	S16 and (first near driver)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:15
S19	11	S18 and (execut\$3 near order)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:15
S20	145	(driver near depend\$3)and bios	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:31
S21	1	(driver near depend\$3 near (order or sequenc\$3))and bios	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:35
S22	578814	(second near driver near depend\$3 near first driver)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:35
S23	1	(second near driver near depend\$3 near first near driver)	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:59
S24	0	"200300056090"	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:59
S25	2	"200300056090"	US-PGPUB; USPAT; USOCR	OR	ON	2008/01/04 17:59

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bios boot sequence

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[System and method for providing technical support of an electronic system through a web bios - all 3 versions »](#)

DE Dent - US Patent 5,884,073, 1999 - Google Patents

... ment of the Web **BIOS boot sequence** that establishes an temporarily ... the Web **BIOS boot sequence** that establishes an intercon- **boot** ...

[Cited by 22 - Related Articles - Web Search](#)

[... to determine cause of failed **boot sequence** to improve likelihood of successful subsequent **boot** ... - all 3 versions »](#)

ED Anderson... - US Patent 5,974,546, 1999 - Google Patents

... The failure of a POST routine will cause the **BIOS** 524 to be unable to ... a series of steps to determine the cause of a previous failed **boot** up **sequence** and to ...

[Cited by 29 - Related Articles - Web Search](#)

[... **BIOS** including multiple option **BIOS** images for multichip support and **boot sequence** for use therewith - all 3 versions »](#)

FA Mahmoud - US Patent 6,425,079, 2002 - Google Patents

... (54) UNIVERSAL OPTION ROM **BIOS** INCLUDING MULTIPLE OPTION **BIOS** IMAGES FOR MULTICHP SUPPORT AND **BOOT SEQUENCE** FOR USE THEREWITH (75 ...

[Cited by 8 - Related Articles - Web Search](#)

[Method and apparatus for controlling network and workstation access prior to workstation **boot** - all 6 versions »](#)

SM Chang - US Patent 5,444,850, 1995 - Google Patents

... operating system software during the **boot sequence**, it performs certain operating system functions by using the basic input/output system (**BIOS**) of the ...

[Cited by 97 - Related Articles - Web Search](#)

[BIOS emulation parameter preservation across computer bootstrapping - all 2 versions »](#)

DD Williams, SL Merkin, CR Dart... - US Patent 5,694,583, 1997 - Google Patents

... FIG. 11 is a flow chart showing a warm **boot sequence** preserving **BIOS** emulation parameters, as practiced in accordance with the invention. FIGS. ...

[Cited by 34 - Related Articles - Web Search](#)

[Extensible bios for boot support of devices on multiple hierarchical buses - all 3 versions »](#)

JL Garney - US Patent 5,854,905, 1998 - Google Patents

... the manage- ment of **boot** devices and the **sequence** of booting is con- trolled through fixed code contained within Basic Input/ ^ Output System (**BIOS**) Read-Only ...

[Cited by 44 - Related Articles - Web Search](#)

[Method and system for selecting from multiple **boot** code images to be loaded in a data processing ... - all 2 versions »](#)

M Aguilar, N Blam, JW Gorrell Jr, YC Lo, JM ... - US Patent 6,446,203, 2002 - Google

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bootstrap and application processor

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The "AND" operator is unnecessary -- we include all search terms by default. [\[details\]](#)

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All Results

[R Bealkowski](#)[J Dyer](#)[D Engler](#)[W Arbaugh](#)[M Newton](#)

Shared memory initialization method for system having multiple processor capability - all 3 versions »

DL Collins - US Patent 6,158,000, 2000 - Google Patents

... 11. The method of claim 10, wherein the POST tasks performed by the **bootstrap processor**while the **application processor** tests the plurality of memory locations ...Cited by 22 - Related Articles - Web Search

[PDF] Application Support Architecture for a High-Performance, Programmable Secure Coprocessor - all 18 versions »

J Dyer, R Perez, SW Smith, M Lindemann - 22nd National Information Systems Security Conference, 1999 - cs.dartmouth.edu

... Physical Security Sensing and Response 486 Processor and PC ... Layer 3: Application Layer 2: System Software/OS Layer 1: Secure **Bootstrap** (rewritable portion ...Cited by 28 - Related Articles - View as HTML - Web Search

Exokernel: an operating system architecture for application-level resource management - all 76 versions »

DR Engler, MF Kaashoek, JO'Toole Jr - Proceedings of the fifteenth ACM symposium on Operating ... , 1995 - portal.acm.org

... library operating systems, as should any "priv- ileged" co-processor state ... for this principle is our belief that distributed, **application**-specific, resource ...Cited by 740 - Related Articles - Web Search

Facility for passing data used by one operating system to a replacement operating system - all 2 versions »

GJ Goss, TS Hirsch, TO Holtey - US Patent 4,799,145, 1989 - Google Patents

... when OPERATING SYSTEM a"bootstrap" operation has been signaled, the input-5/output

(I/O) **processor** takes control. Generally it has This **application** is a ...Cited by 22 - Related Articles - Web Search

[PDF] Parametrised processor generation - all 4 versions »

I Page - More FPGAs. Abingdon EE&CS books, 1994 - doc.ic.ac.uk

... **Bootstrap** facilities are added as required ... show a sample of the output of a **processor** generator, but ... The following example is a simple **application** program that ...Cited by 15 - Related Articles - View as HTML - Web Search

Secure and reliable bootstrap architecture - all 28 versions »

WA Arbaugh, DJ Farber, AD Keromytis, JM Smith - US Patent 6,185,678, 2001 - Google Patents

... **PROCESSOR** 4 ... SECURE AND RELIABLE **BOOTSTRAP** ARCHITECTURE CROSS-REFERENCE TO RELATED**APPLICATION** This **application** claims the benefit of earlier filed US ...Cited by 258 - Related Articles - Web Search